

Docket No. 0057-2533-2YY CONT

IN RE APPLICATION OF: Hideki TAKAHASHI
SERIAL NO: 09/421,217

RCE FILED: February 7, 2002

FOR: INSULATED GATE SEMICONDUCTOR DEVICE WITH LOW ON VOLTAGE AND MANUFACTURING
METHOD THEREOF



ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith: Marked-Up Copy

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	18	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	2	MINUS	3	0	× \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS		+ \$280 =		\$0.00
						\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment		+ \$40 =		\$0.00
						TOTAL \$0.00

- A check in the amount of _____ is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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